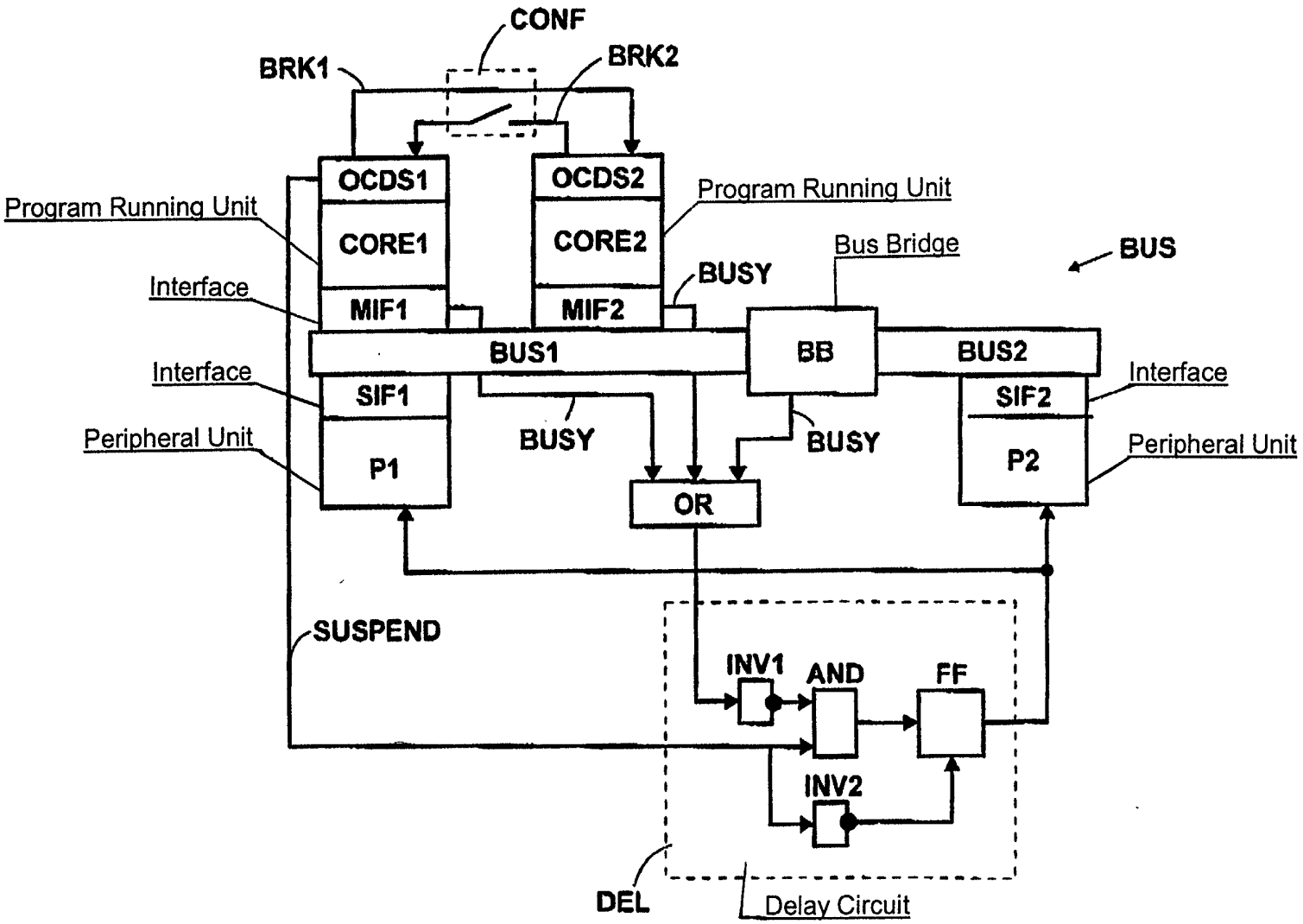
CONF
BRK1
BRK2
OCDS1
OCDS2
Program Running Unit
Program Running Unit
CORE1
CORE2
Bus Bridge
BUS
BUSY
Interface
MIF1
MIF2
BUS1
BB
BUS2
Interface
Interface
SIF1
SIF2
BUSY
BUSY
Peripheral Unit
P1
P2
Peripheral Unit
OR
SUSPEND
INV1
AND
FF
INV2
DEL
Delay Circuit